

06-13-05

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EXPRESS MAIL LABEL NO. EV516209299US

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[TS01-1194]

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Shu-Ying Cho

Confirmation No. 9792

Serial No.: 10/754,833

Group Art Unit: 2826

Filed: January 9, 2004

Examiner: Johannes P. Mondt

For: **TEMPORARY SELF-ALIGNED STOP LAYER IS APPLIED ON SILICON
SIDEWALL**

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Steven E. Koffs, Registration No. 37,163

MAIL STOP Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

Dear Sir:

This is in response to the Restriction Requirement dated May 13, 2005. The Examiner requires an election between Group I, Claims 1-25, drawn to a method of forming a MOS transistor, classified in class 438, subclass 197+, or Group II, Claims 26-38, drawn to a MOS transistor, classified in class 257, subclass 288+.

Applicant hereby elects Group I, Claims 1-25, drawn to a method of forming a MOS transistor. This election is made without traverse.

DATE:

6/10/05

Respectfully submitted,

Steven E. Koffs

Steven E. Koffs

Registration No. 37,163

Attorney for Applicant(s)

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